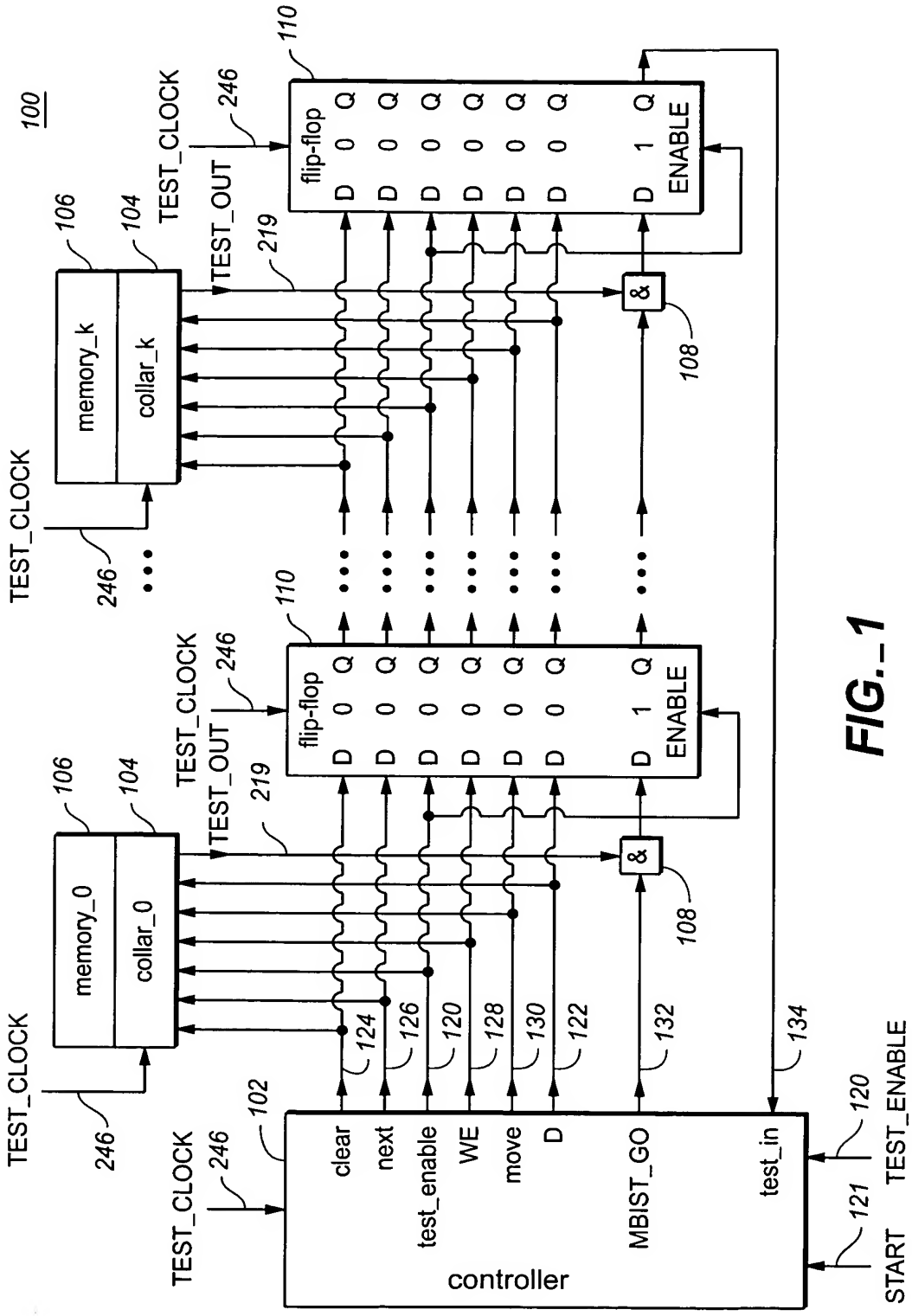


FIG._1

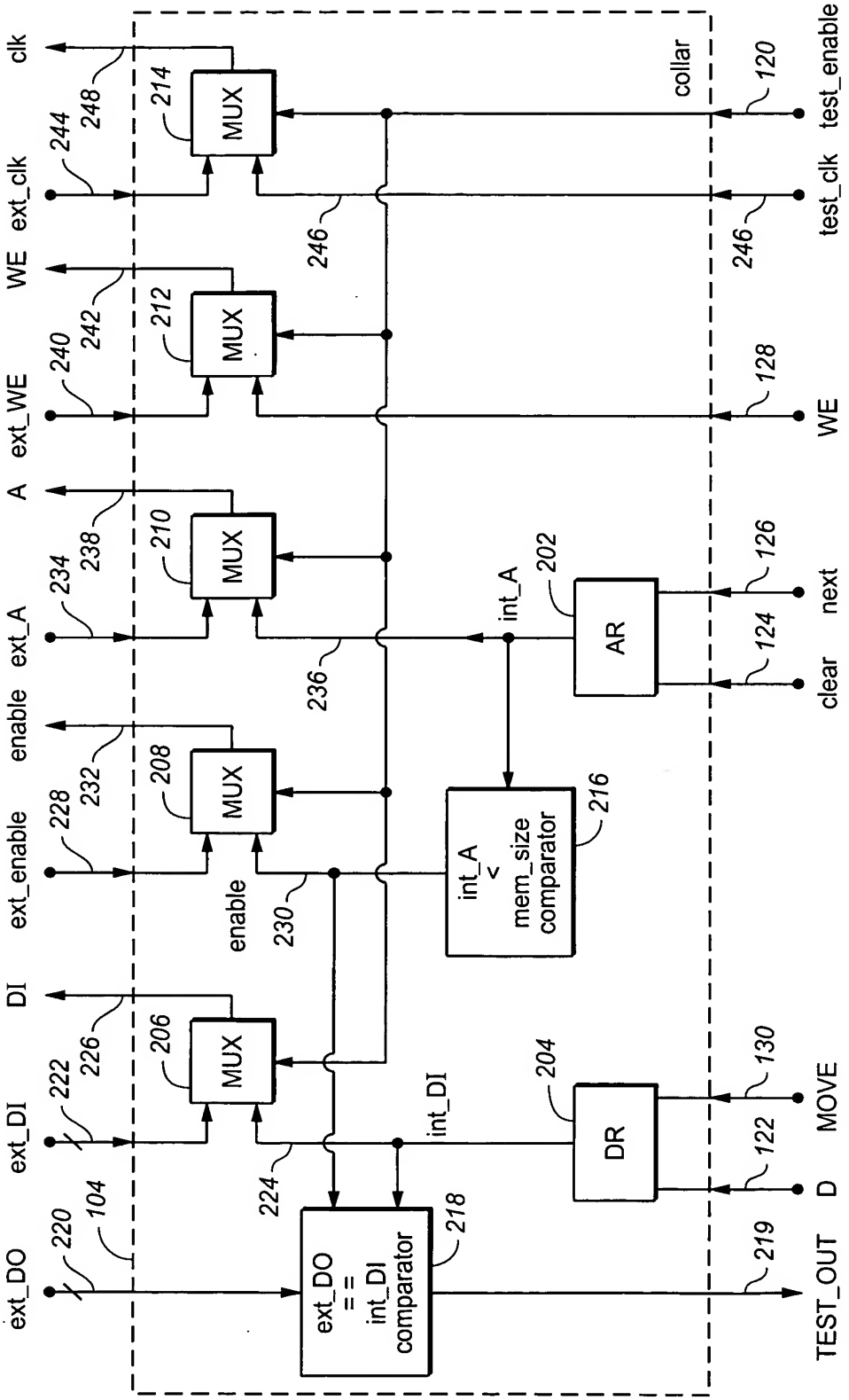


FIG._2

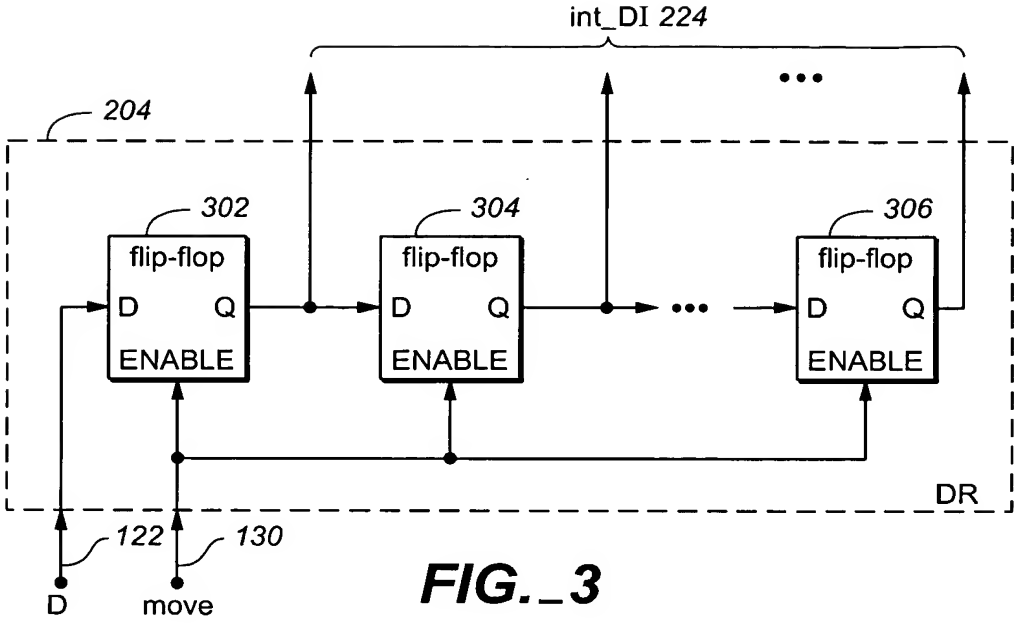


FIG._3

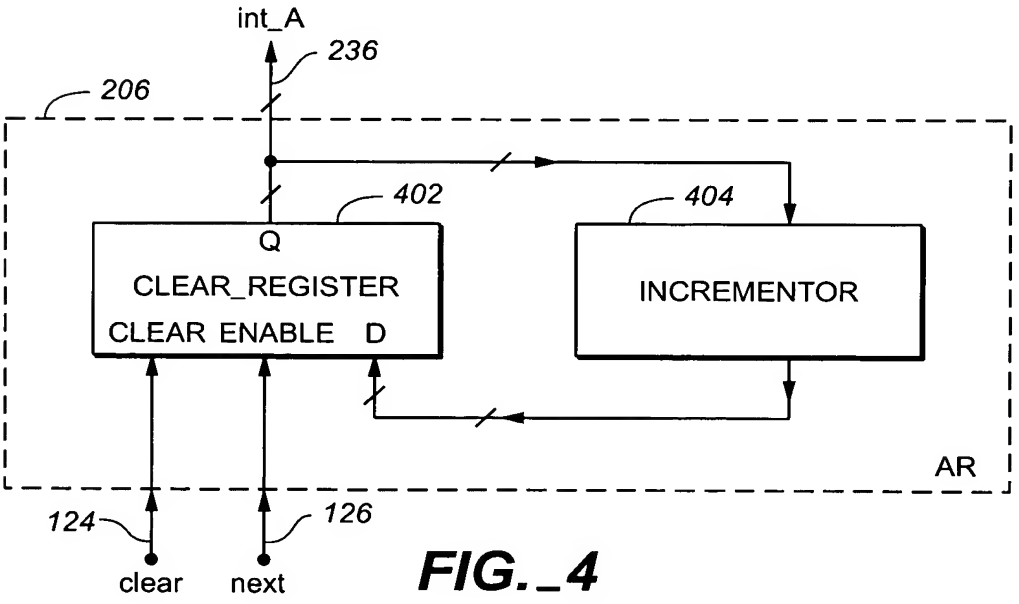


FIG._4

FIG._5A

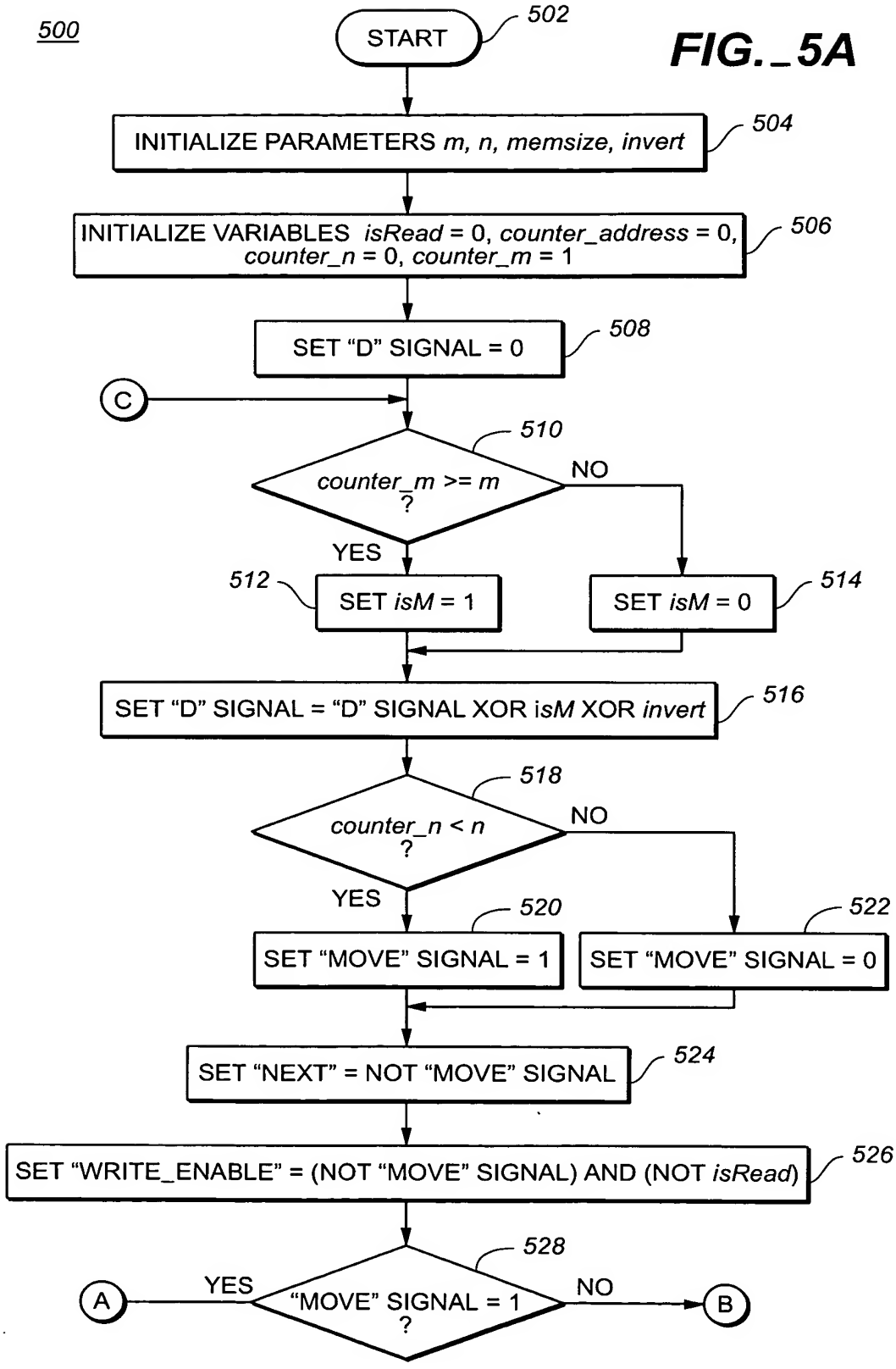
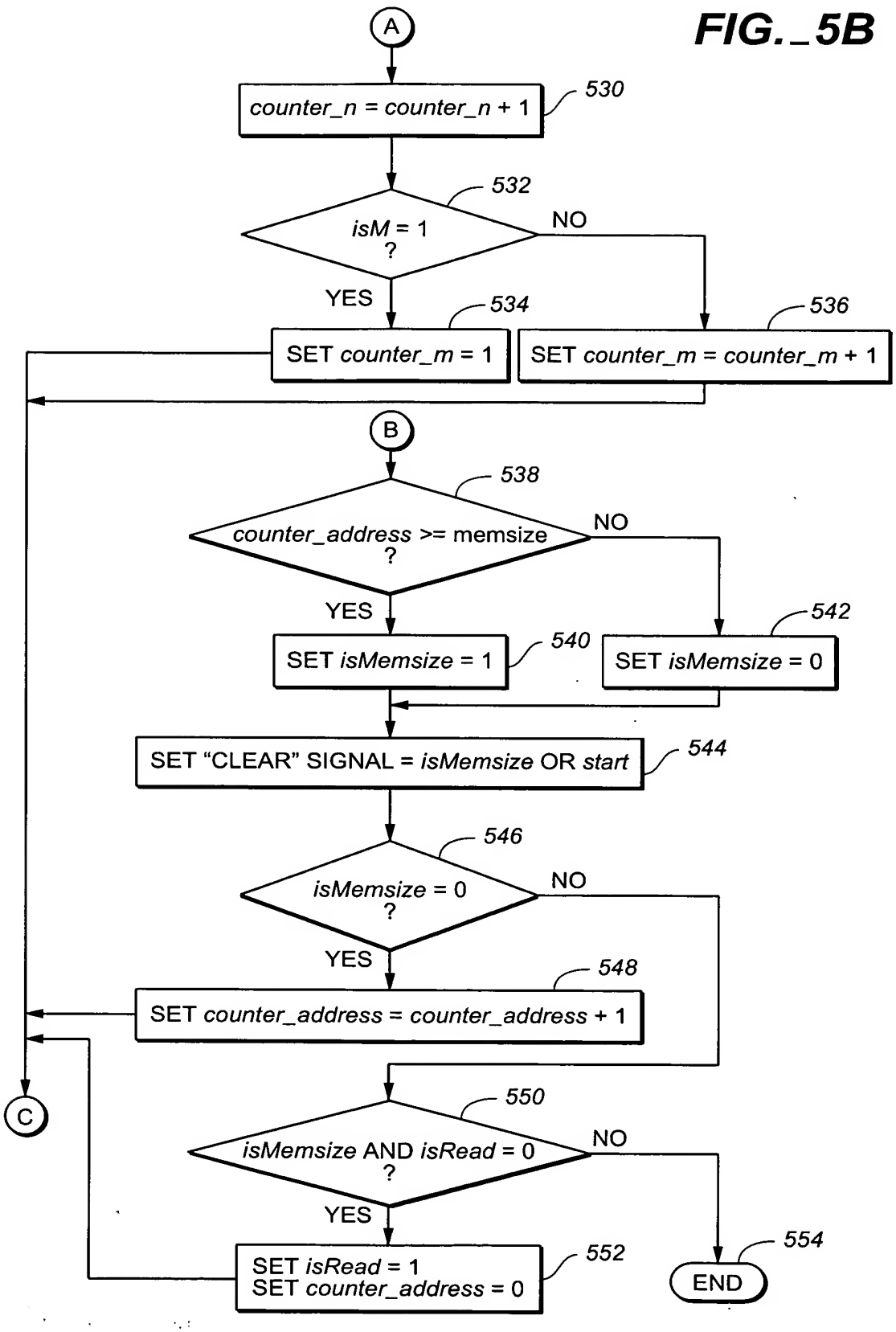


FIG._5B



	602 fill DR(n steps)	604 memory write (memsize steps)	606 memory read (memsize steps)
clear	1 0 0 ••• 0	0 0 ••• 0	1 0 ••• 0
move	1 1 1 ••• 1	0 0 ••• 0	0 0 ••• 0
next	0 0 0 ••• 0	1 1 ••• 1	1 1 ••• 1
WE	0 0 0 ••• 0	1 1 ••• 1	0 0 ••• 0
D	0 ••• 0 1 ••• 1 0 ••• 0 α	α ⊕ is M ••• α ⊕ is M	α ⊕ is M ••• α ⊕ is M
	m m m		

FIG._6

	802 fill DR(n steps)	804 memory write memsize+[memsize/k]steps)					806 fill DR(n steps)	808 memory read memsize+[memsize/k]steps)				
clear	1 0 0 0 ••• 0	0 ••• 0					1 0 0 ••• 0	0 ••• 0				
move	1 1 1 1 ••• 1	0 ••• 0	1	0 ••• 0	1	•••	1 ••• 1	0 ••• 0	1	0 ••• 0	1	•••
next	0 0 0 0 ••• 0	1 ••• 1	0	1 ••• 1	0	•••	0 ••• 0	1 ••• 1	0	1 ••• 1	0	•••
WE	0 0 0 0 ••• 0	1 ••• 1	0	1 ••• 1	0	•••	0 ••• 0	0 ••• 0				
D	0 1 0 1 ••• $\bar{\alpha}$ α	α ••• α	$\bar{\alpha}$	$\bar{\alpha}$ ••• $\bar{\alpha}$	α	•••	0 1 0 1 ••• α	α ••• α	$\bar{\alpha}$	$\bar{\alpha}$ ••• $\bar{\alpha}$	α	•••
	n	k		k			n	k		k		

FIG._8

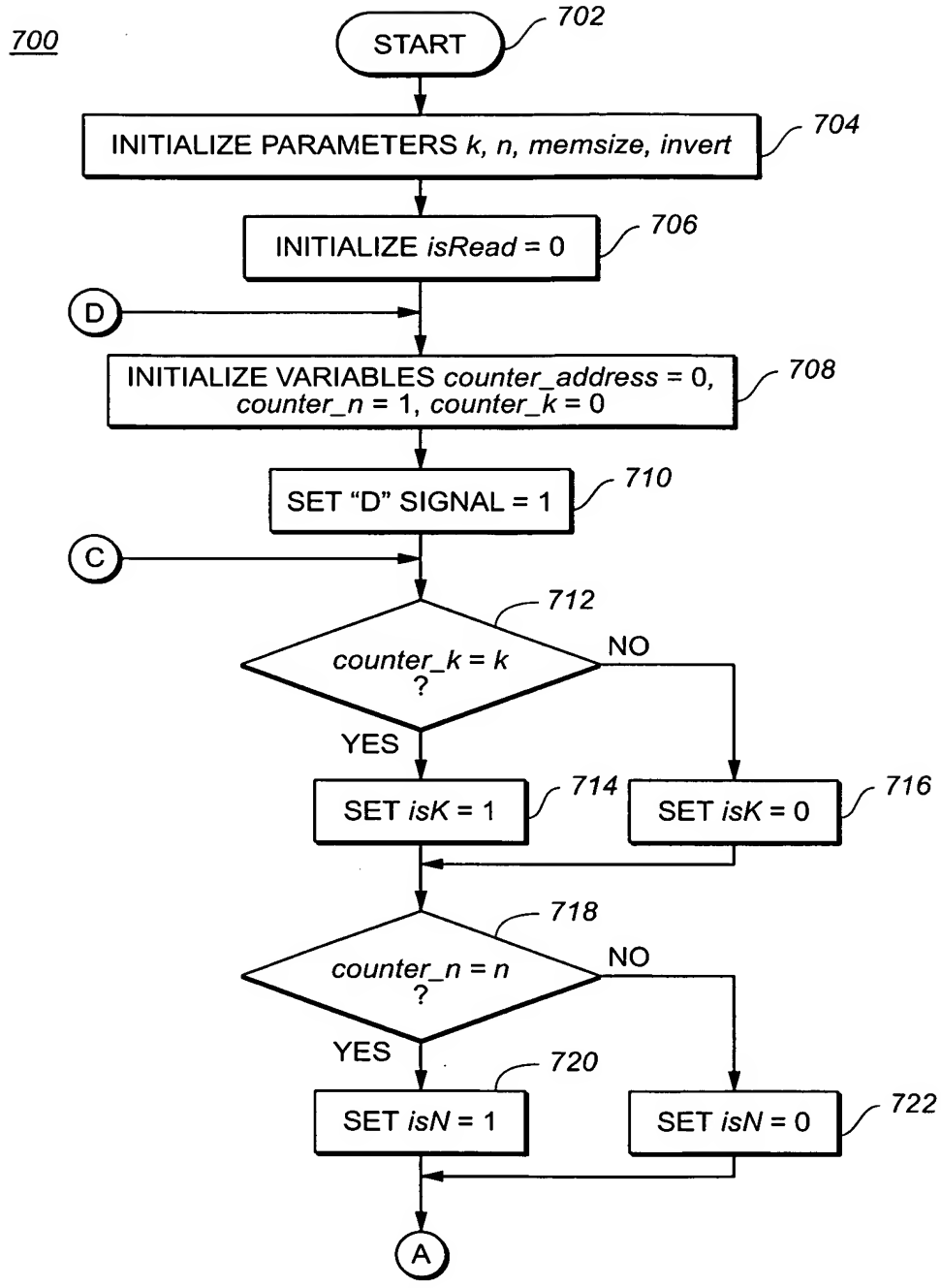


FIG._7A

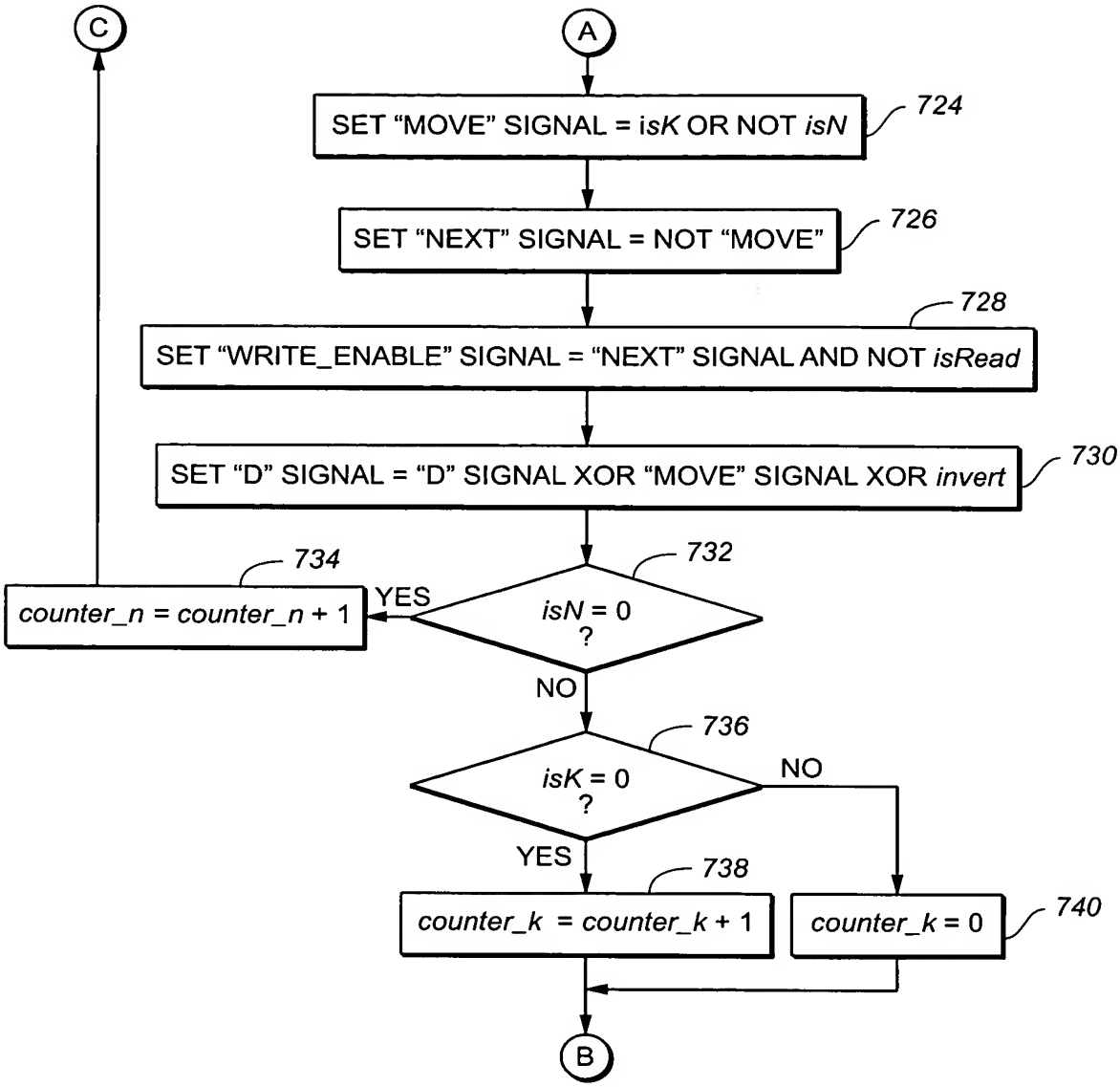


FIG._7B

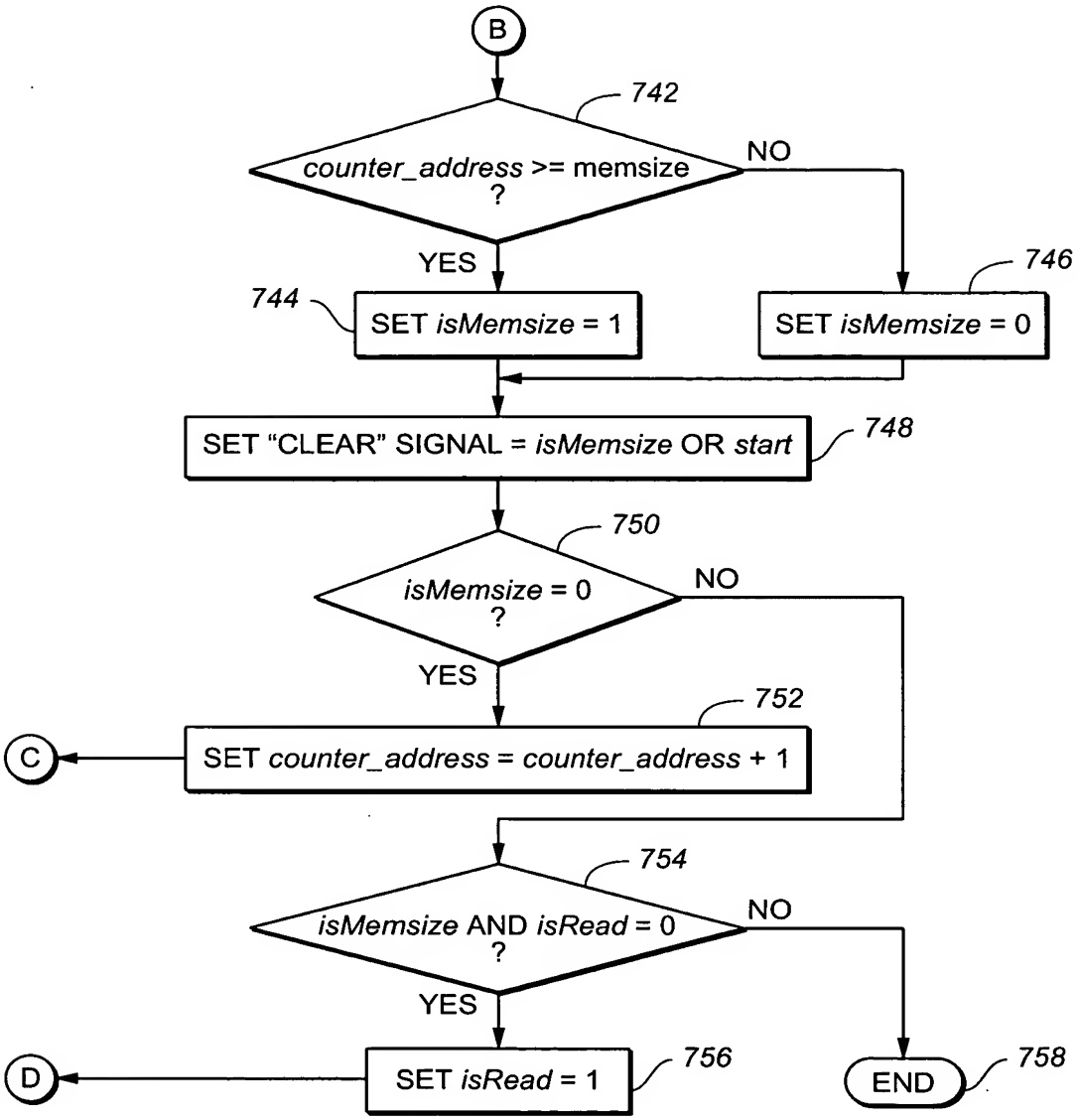


FIG._7C